PTO/SB/ 44 (04-05)
Approved for use through 04/30/2007 OMB 0651-0033
Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page	1	of	1

PATENT NO .:

6937047 B2

APPLICATION NO:

10/634484

DATE:

08-05-2003

FIRST NAMED INVENTOR:

TRAN, TU-ANH

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 6. Line 21, Claim No. 7:

Change "(BIST) circuitry" to (BIST) circuitry-

In Column 6, Line 28, Claim No. 9:

Change "100 μ mx200" to—100 μ m x 200—

In Column 6, Line 37, Claim No. 12:

Change "one lest pad" to -one test pad-

In Column 7. Line 25. Claim No. 28:-

Ghange "plurality bond pads" to - plurality of bond pads.

NOL

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.

Law Department

7700 West Parmer Lane PL02

Austin, TX 78729

Copy VI